1

L Number	Hits	Search Text	T D D	I mi ma a ta
1	1249		DB USPAT;	Time stamp 2003/04/16 16:02
1	1245	(/1//124-136).0013.	US-PGPUB;	2003/04/16 16:02
			EPO; JPO;	
1			DERWENT;	
			IBM TDB	
i _	2	("5564019").PN.	USPAT;	2003/04/15 09:30
	_	( 3301013 )	US-PGPUB;	2003/04/13 03.30
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	2	("5551013").PN.	USPAT;	2003/04/15 13:55
		, , , , , , , , , , , , , , , , , , , ,	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	19504441	@ad<=20000906 or @rlad<=20000906	USPAT;	2003/04/16 10:27
	,		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	209		USPAT;	2003/04/15 10:44
		or array) adj4 (processors or module\$1 or	US-PGPUB;	
		element\$1)) or FPGA) and emulat\$5.ab.	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000/04/15
-	503		USPAT;	2003/04/15 10:46
		(emulat\$5 or cosimulat\$5) and (reprogram\$6	US-PGPUB;	
		or reconfigur\$6)	EPO; JPO;	
			DERWENT;	
	51	((block\$4 near3 (code or transfer\$3)) same	IBM_TDB USPAT;	2003/04/15 11:06
_	31	( memory and (processor\$1 or module\$1 or	US-PGPUB;	2003/04/15 11:06
		element\$1 or chip\$1))) and (emulat\$4 or	EPO; JPO;	
-		cosimulat\$4).ab.	DERWENT;	
		- CODIMULACT 17 . 42.	IBM TDB	
_	7	((block\$4 or unblock\$4) near4 transfer\$4)	USPAT;	2003/04/15 10:56
1		and (compil\$5 and FPGA) and (emulat\$4 or	US-PGPUB;	
		cosimulat\$4).ab.	EPO; JPO;	
			DERWENT;	-
			IBM_TDB	
-	50		USPAT;	2003/04/15 18:40
		(memory or host))) ) and (emulation or	US-PGPUB;	
		cosimulation) and ( (compil\$4 or decod\$4)	EPO; JPO;	
		near4 code)) and ( FPGA or reprogramm\$5 or	DERWENT;	
	100005	reconfigurab\$5)	IBM_TDB	0000/04/45
-	190025	(block\$4 and unblock\$4 A) near5 (code or	USPAT;	2003/04/15 11:47
		instruction\$1 or module)	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM TDB	
_	117	(data near3 transfer\$4) same ((processor\$1	USPAT;	2003/04/15 15:40
	***	or element\$1) near5 (host or "work	US-PGPUB;	
		station")) and (reprogrammable or FPGA)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	41	(((lock\$4 or prohibit\$4 or stop\$4 or	USPĀT;	2003/04/16 10:26
		inhibit\$4) or (unlock\$4 or restor\$4 or	US-PGPUB;	
		restart\$4 or unblock\$4)) near4 (	EPO; JPO;	
		transfer\$3 near2 data) ) and ( code near4	DERWENT;	
	_	compil\$4)	IBM_TDB	0000/04/15 15 15
-	6	((((lock\$4 or prohibit\$4 or stop\$4 or	USPAT;	2003/04/15 15:46
	ļ	inhibit\$4) or (unlock\$4 or restor\$4 or restart\$4 or unblock\$4)) near4 (	US-PGPUB;	
		transfer\$3 near2 data) ) and ( code near4	EPO; JPO;	
	ļ	compil\$4)) and ( FPGA or "processing	DERWENT; IBM TDB	
	ļ	elements" or "processor chips" or (array	1011-101	
		near3 processors)) and (emulat\$4 or		
		cosimulat\$4)		

-	1926		USPAT;	2003/04/15 17:38
1		bit )) and ( (program or code) same	US-PGPUB;	
		compil\$4) and (memory near4 (control or	EPO; JPO;	
		access))	DERWENT;	
		50010.15	IBM_TDB	
-	90		USPAT	2003/04/15 16:11
-	1481	(716/4,5; 703/26).CCLS.	USPAT;	2003/04/15 16:55
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		4471644 5 7004061 2007	IBM_TDB	
_	18		USPAT;	2003/04/15 17:37
		((array or network) near4 (processors or	US-PGPUB;	ļ.
		element\$1 or chips))) and (emulat\$4 or	EPO; JPO;	
		cosimulat\$4) and (compil\$4 same program)	DERWENT;	
		///716/A E. 702/20\ CGTC \ ===	IBM_TDB	0000/04/15 17 50
-	55	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2003/04/15 17:52
		((((716/4,5; 703/26).CCLS.) and (FPGA or	US-PGPUB;	
		((array or network) near4 (processors or	EPO; JPO;	
		element\$1 or chips))) and (emulat\$4 or	DERWENT;	
		cosimulat\$4) and (compil\$4 same program)) and 717/\$)) and ((memory adj2 access\$4)	IBM_TDB	
		or (bus near3 priority)) and (control		
		near8 (bit\$1 or mask))	İ	
_	15323		USPAT;	2003/04/15 17:37
-	15525	and (disabl\$4 or lock\$4 or (turn adj off)	US-PGPUB;	2003/04/13 17:37
		or inhibit\$4)) same (( data near3	EPO; JPO;	
		transfer\$4) or (memory near4 ( read or	DERWENT;	
		write or access or store)) or (bus adj2	IBM TDB	
		priority))	1011_100	
_	95		USPAT;	2003/04/15 17:37
		and (disabl\$4 or lock\$4 or (turn adj off)	US-PGPUB;	2003,01,13 17.37
		or inhibit\$4)) same ((data near3	EPO; JPO;	
		transfer\$4) or (memory near4 ( read or	DERWENT;	
		write or access or store)) or (bus adj2	IBM TDB	
		priority))) and ( FPGA or ((array or	_	
		network) near4 (processors or element\$1 or	ļ	
		chips))) and (emulat\$4 or cosimulat\$4)		
	1	and (compil\$4 same program)		
i -	2834	((control\$4 or disabl\$4 or on or off or	USPAT;	2003/04/15 17:39
		enable ) near5 (mask\$1 or bit\$1 )) and (	US-PGPUB;	
		(program or code) same compil\$4)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	51	(maintenance adj2 bus ) and (	USPAT;	2003/04/16 10:33
		multiprocess\$4 or FPGA or (host and	US-PGPUB;	
		processors)) and ( ((memory or bus) near3	EPO; JPO;	
		(access or preempt\$5)) same (right\$1 or	DERWENT;	
		privile\$6 or priorit\$4))	IBM_TDB	
-	14	((maintenance adj2 bus ) and (	USPAT;	2003/04/16 10:31
		multiprocess\$4 or FPGA or (host and	US-PGPUB;	
		processors)) and ( ((memory or bus) near3	EPO; JPO;	
[		(access or preempt\$5)) same (right\$1 or	DERWENT;	ĺ
		privile\$6 or priorit\$4))) and (host and	IBM_TDB	
		((maint\$7 or clean\$3 or recover\$4 or		
] _	3.4	updat\$4) near4 (activit\$4 or task\$4)))	IICDAM -	2002/04/16 11-04
-	34	((maintenance adj2 bus ) and (	USPAT;	2003/04/16 11:04
		multiprocess\$4 or FPGA or (host and	US-PGPUB;	
		processors)) and ( ((memory or bus) near3	EPO; JPO;	
		(access or preempt\$5)) same (right\$1 or	DERWENT;	
		<pre>privile\$6 or priorit\$4))) and ( emulat\$4 or cosimulat\$4)</pre>	IBM_TDB	
		OT COSTUMITACAA)		

	1	1 / / / C124F1C A	T-12-22-2	1 0000 /04 /15 15 00
-	1	((US-6134516-\$ or US-6009256-\$ or	USPAT;	2003/04/16 16:00
		US-5841967-\$ or US-6349392-\$ or	US-PGPUB;	
		US-5410652-\$ or US-6092097-\$ or	EPO; JPO;	
		US-5966528-\$ or US-5963746-\$ or	DERWENT;	
		US-5551013-\$ or US-5963736-\$ or	IBM_TDB	
	1	US-5960191-\$ or US-5911059-\$ or		
	1	US-6122706-\$ or US-5796961-\$ or		
		US-6336154-\$ or US-5021945-\$ or		
		US-4885684-\$ or US-6480952-\$ or	i	
		US-6480818-\$ or US-6389379-\$ or		
		US-6480845-\$ or US-5016162-\$ or		
		US-4000485-\$ or US-5666515-\$ or		
		US-5592673-\$ or US-6522985-\$).did. or		
		(US-5805792-\$ or US-5329471-\$ or		
		US-5966534-\$).did. or (EP-207320-\$).did.)		
		and (maintenance adj2 bus ) and (		
		multiprocess\$4 or FPGA or (host and		
		processors))		
_	32	(((maintenance adj2 bus ) and (	USPAT;	2003/04/16 11:55
	32	multiprocess\$4 or FPGA or (host and	US-PGPUB;	2003/04/10 11:33
		processors)) and ( ((memory or bus) near3	EPO; JPO;	
		(access or preempt\$5)) same (right\$1 or	DERWENT;	
		privile\$6 or priorit\$4))) and (emulat\$4	IBM TDB	
		, •	TDM_1DD	
		or cosimulat\$4)) and ( FPGA or		
	,	multiprocess\$5 )	IIO DAM.	2002/04/16 16:02
-	1	((((maintenance adj2 bus ) and (	USPAT;	2003/04/16 16:02
		multiprocess\$4 or FPGA or (host and	US-PGPUB;	
		processors)) and (((memory or bus) near3	EPO; JPO;	
		(access or preempt\$5)) same (right\$1 or	DERWENT;	
		<pre>privile\$6 or priorit\$4))) and ( emulat\$4</pre>	IBM_TDB	
		or cosimulat\$4)) and (FPGA or		
		multiprocess\$5 )) and ( DSP or HDL or ASIC		